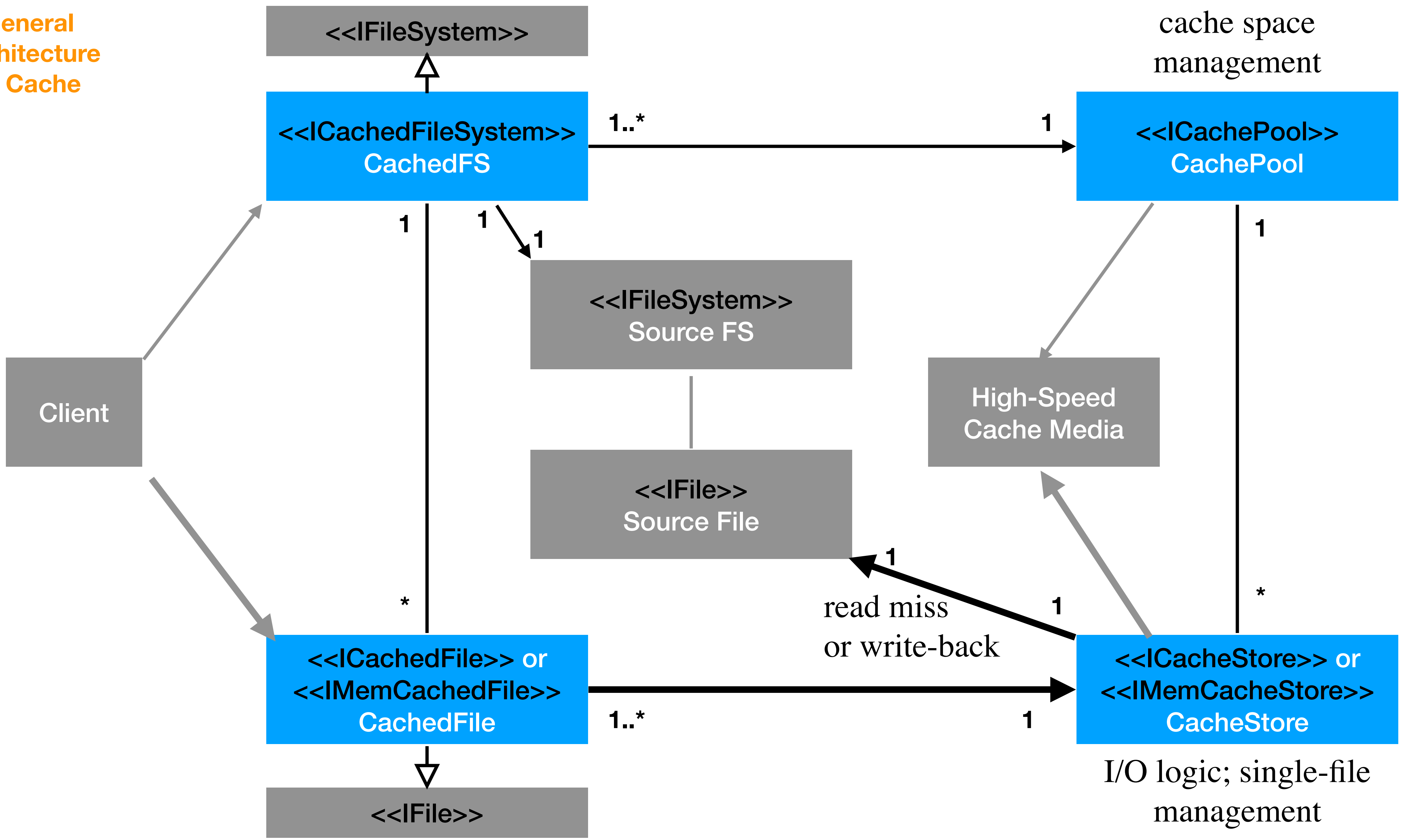
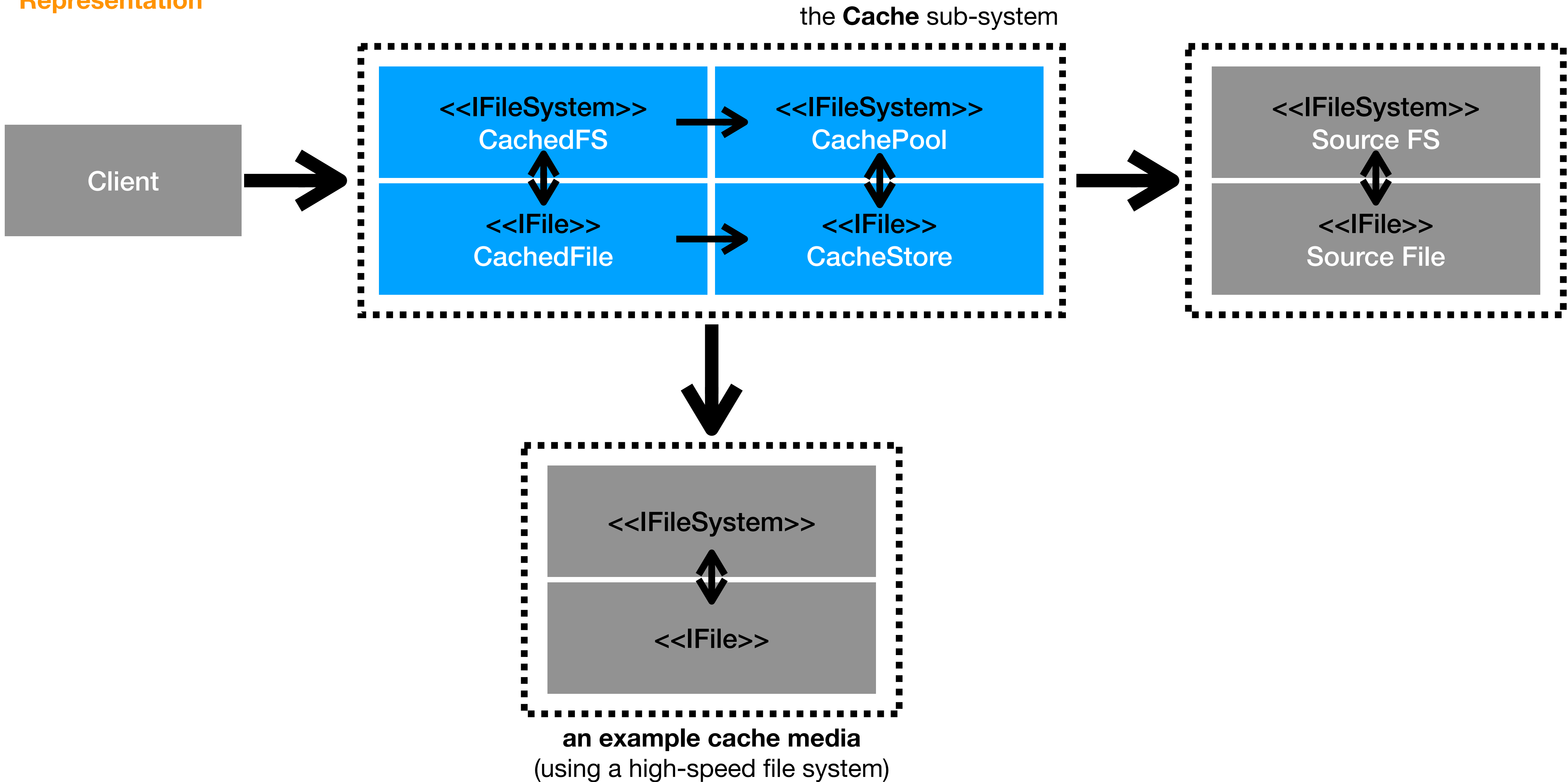


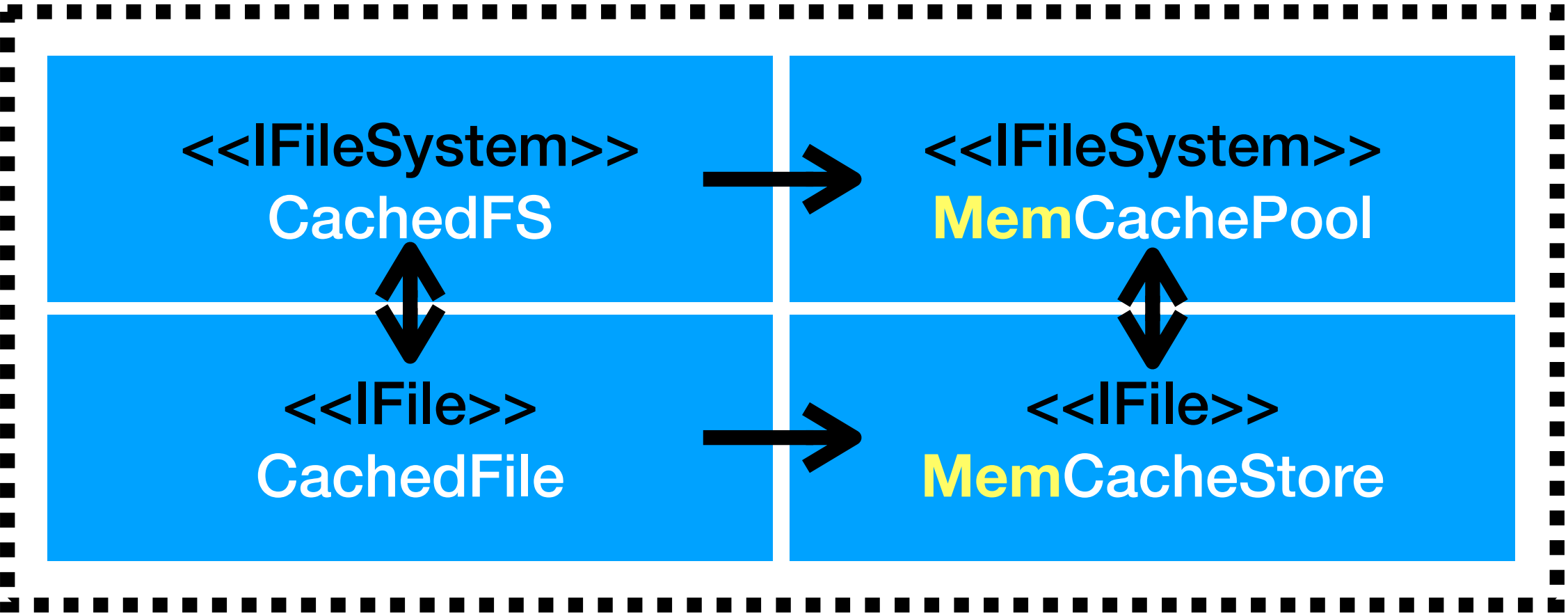
General
Architecture
of Cache



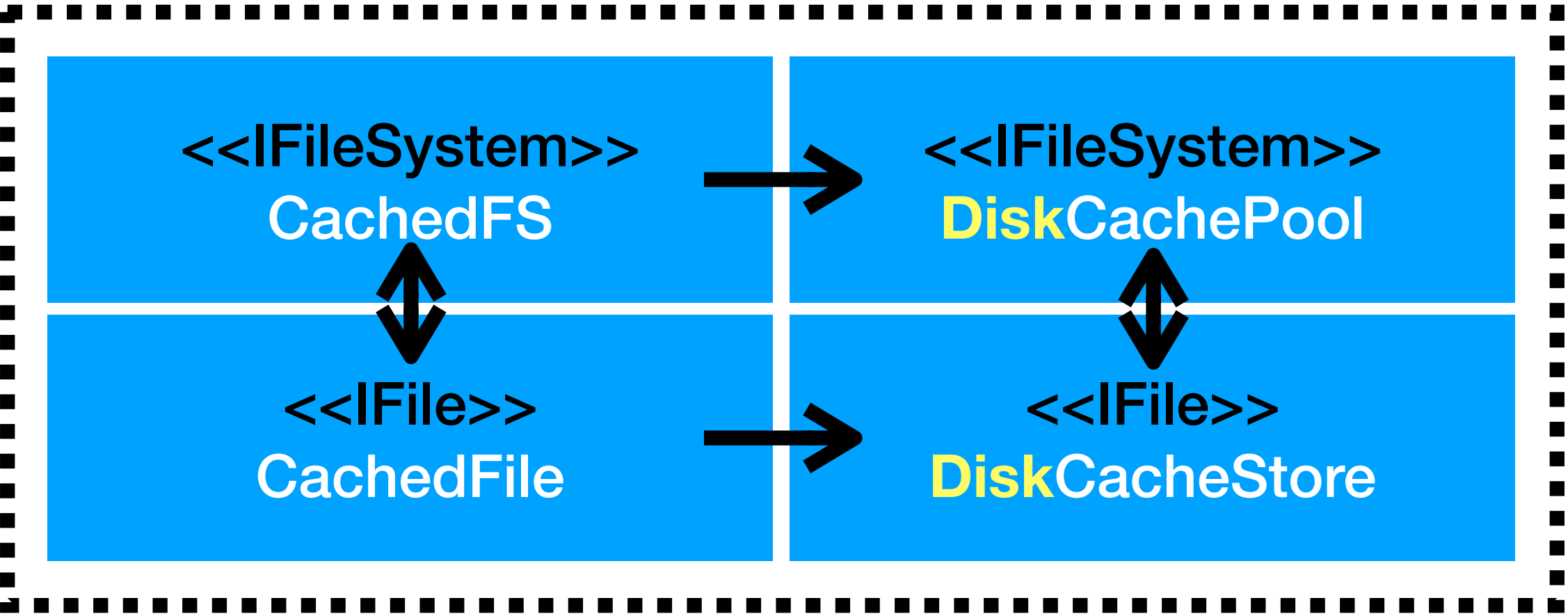
**Simplified
Representation**



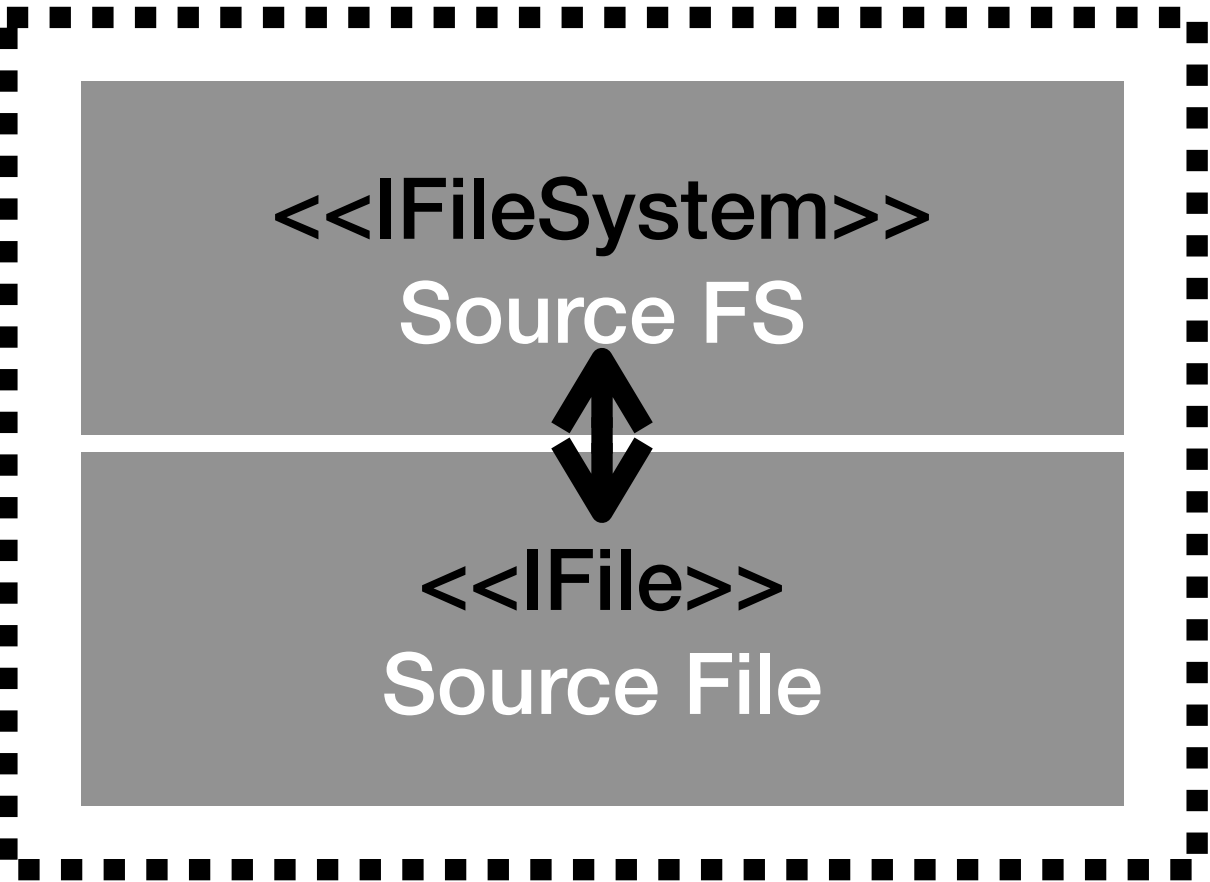
Possible
Chained Tiering



L1 Cache



L2 Cache



Possible
Native Tiering

